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7590

04/29/2005

LeMOINE PATENT SERVICES, PLLC
 C/O PORTFOLIOIP
 P.O. BOX 52050

MINNEAPOLIS, MN 55402

07/28/2005 WASFAWE 00000131 09917661

01 FC:1501 1400.00 OP
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Chris Hammond (Depositor's name)
 Chris Hammond (Signature)
 July 25, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/917,661	07/31/2001	Praveen K. Parvathala	219.39515X00	1090

TITLE OF INVENTION: FUNCTIONAL RANDOM INSTRUCTION TESTING (FRIT) METHOD FOR COMPLEX DEVICES SUCH AS MICROPROCESSORS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	07/29/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
TABONE JR, JOHN J	2133	714-030000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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1. LeMoine Patent
 2. Services, PLLC
 3.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Intel Corporation

Santa Clara, California

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature Dana B LeMoineDate 7-25-05Typed or printed name Dana B LeMoineRegistration No. 40062

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Praveen Parvathala

Title: FUNCTIONAL RANDOM INSTRUCTION TESTING (FRIT) METHOD FOR COMPLEX DEVICES SUCH AS MICROPROCESSORS

Docket No.: 80107.051US1

Filed: July 31, 2001

Examiner: John J. Tabone

Serial No.: 09/917,661

Due Date: July 29, 2005

Group Art Unit: 2133

Confirmation No.: 1090

Commissioner for Patents
Attn: MAIL STOP ISSUE FEE
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Notice of Allowance Date:
April 29, 2005

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- ☒ Issue Fee Transmittal (Form PTOL-85).
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By Dana B. LeMoine
Dana B. LeMoine
Reg. No. 40,062
DBL/clh

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Chris Hammond
Name

Chris Hammond
Signature